Industrial Pathways to Scalable, High-Performance Silicon PV: Interfaces, Metallisation, and Wafer Innovations for Terawatt Manufacturing

June 3rd
09:00-12:00 CET
Online

Time	Topic	Presenters
09:00 - 09:05	*Introduction*	SiLEAN Team
09:05 - 09:30	Opening presentation on the pathways to scaling sustainable, high-efficiency silicon PV in Europe	**Thomas Garabetian**, *Solar Power Europe*
09:30 - 09:55	Scaling and demonstrating novel back-end equipment and processes for SHJ and TOPCon solar cells	**Victor Acinas**, *Applied Materials Europe, SHINE-PV Coordinator*
09:55 - 10:20	Alternative equipment and processes for Si PV manufacturing: An opportunity to bring PV production back to Europe	**Junjie Zhu**, *Institute for Energy Technology, EMPOWER Coordinator*
	10:20 - 10:30 Coffee break	
10:30 - 10:45	Pioneering critical-raw material free and ultra-thin silicon heterojunction solar cells: a status of SiLEAN research	**Valerie Depauw**, *Research Engineer at IMEC*
10:45 - 11:00	Front side texturing strategies for BC solar cells in the BURST project	**Laurent Clochard**, *CTO of Nines PV*
11:00 - 11:15	Towards Terawatt Production of c-Si Solar Photovoltaics: the project TERASUN	**Alessia Bezzon**, *Surface Technologies Consultant at RINA Consulting*
11:15 - 12:00	*Q&A and round table discussion on scaling challenges, industrial pathways, and supply chain resilience for next-generation silicon PV technologies*	*All speakers* Moderator: **Delfina Munoz**, *CEA/ETIP-PV*

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