


Industrial Pathways to Scalable, High-Performance Silicon PV: Interfaces, Metallisation, and Wafer Innovations for Terawatt Manufacturing

June 3rd
09:00-12:00 CET
Online



Time	Topic	Presenters
09:00 - 09:05	Introduction	SiLEAN Team
09:05 - 09:30	Opening presentation on the pathways to scaling sustainable, high-efficiency silicon PV in Europe	Thomas Garabetian , <i>Solar Power Europe</i>
09:30 - 09:55	Scaling and demonstrating novel back-end equipment and processes for SHJ and TOPCon solar cells	Victor Acinas , <i>Applied Materials Europe</i> , <i>SHINE-PV Coordinator</i>
09:55 - 10:20	Alternative equipment and processes for Si PV manufacturing: An opportunity to bring PV production back to Europe	Junjie Zhu , <i>Institute for Energy Technology</i> , <i>EMPOWER Coordinator</i>
<i>10:20 - 10:30 Coffee break</i> 		
10:30 - 10:45	SiLEAN project highlights and pathways to industrial scale-up	Valerie Depauw , <i>Research Engineer at IMEC</i>
10:45 - 11:00	BURST project highlights and pathways to industrial scale-up	Laurent Clochard , <i>CTO of Nines PV</i>
11:00 - 11:15	TERASUN project highlights and pathways to industrial scale-up	tbc
11:15 - 12:00	Q&A and round table discussion on scaling challenges, industrial pathways, and supply chain resilience for next-generation silicon PV technologies	<i>All speakers</i> Moderator: Delfina Munoz , <i>CEA/ETIP-PV</i>

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